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## NTE4529B Integrated Circuit CMOS, Dual 4-Channel Analog Data Selector

**Description:**

The NTE4529B analog data selector is a dual 4-channel or single 8-channel device depending on the input coding. This 16-Lead DIP device is suitable for digital as well as analog applications, including various one-of-four and one-of-eight data selector functions. Since the device has bidirectional analog characteristics it can also be used as a dual binary to 1-of-4 or a binary 1-of-8 decoder.

**Features:**

- Data Paths are Bidirectional
- Quiescent Current = 1nA/Package (Typ) at 5Vdc
- 10Mhz Operation (Typ)
- 3-State Operation
- Linear "On" Resistance
- "On" Resistance 120Ω (Typ) at 15V
- Low Noise – 12nV/√Cycle, f ≥ 1kHz (Typ)
- Supply Voltage Range: 3Vdc to 18Vdc
- Capable of Driving Two Low-Power TTL Loads, One Low-Power Schottky TTL Load or Two Loads Over the Rated Temperature Range

**Absolute Maximum Ratings:** (Voltages referenced to V<sub>SS</sub>)

DC Supply Voltage, V <sub>DD</sub> .....	-0.5 to +18.0V
Input Voltage (All Inputs), V <sub>in</sub> .....	-0.5 to V <sub>DD</sub> to +0.5V
DC Current Drain (Per Pin), I .....	10mA
Operating Temperature Range, T <sub>A</sub> .....	-55° to +125°C
Storage Temperature Range, T <sub>stg</sub> .....	-65° to +150°C

Note 1. This device contains circuitry to protect the control inputs against damage du to high static voltages or electric fields; however, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high-impedance circuit. A destructive high-current mode may occur if V<sub>in</sub> or V<sub>out</sub> is not constrained to the range V<sub>S</sub> ≤ V<sub>in</sub> or V<sub>out</sub> ≤ V<sub>DD</sub>.

## Electrical Characteristics: (Note 2)

Parameter	Symbol	V <sub>SS</sub> Vdc	V <sub>DD</sub> Vdc	-55°C		+25°C			+125°C		Unit
				Min	Max	Min	Typ	Max	Min	Max	
Output Voltage V <sub>in</sub> = V <sub>DD</sub> or 0  V <sub>in</sub> = 0 or V <sub>DD</sub>	"0" Level V <sub>OL</sub>	0.0	5.0	-	0.05	-	0	0.05	-	0.05	Vdc
			10	-	0.05	-	0	0.05	-	0.05	Vdc
			15	-	0.05	-	0	0.05	-	0.05	Vdc
	"1" Level V <sub>OH</sub>	0.0	5.0	4.95	-	4.95	5.0	-	4.95	-	Vdc
			10	9.95	-	9.95	10	-	9.95	-	Vdc
			15	14.95	-	14.95	15	-	14.95	-	Vdc
Input Voltage (Note 4) (V <sub>O</sub> = 4.5 or 0.5Vdc) (V <sub>O</sub> = 9.0 or 1.0Vdc) (V <sub>O</sub> = 13.5 or 1.5Vdc)  (V <sub>O</sub> = 0.5 or 4.5Vdc) (V <sub>O</sub> = 1.0 or 9.0Vdc) (V <sub>O</sub> = 1.5 or 13.5Vdc)	"0" Level V <sub>IL</sub>	0.0	5.0	-	1.5	-	2.25	1.5	-	1.5	Vdc
			10	-	3.0	-	4.50	3.0	-	3.0	Vdc
			15	-	4.0	-	6.75	4.0	-	4.0	Vdc
	"1" Level V <sub>IH</sub>	0.0	5.0	3.5	-	3.5	2.75	-	3.5	-	Vdc
			10	7.0	-	7.0	5.50	-	7.0	-	Vdc
			15	11.0	-	11.0	8.25	-	11.0	-	Vdc
Input Current	I <sub>in</sub>	0.0	15	-	±0.1	-	±0.00001	±0.1	-	±0.1	µAdc
Input Capacitance (V <sub>IN</sub> = 0) Control Switch Input Switch Output Feed Through	C <sub>in</sub>	0.0	-	-	-	-	5.0	7.5	-	-	pF
			-	-	-	-	8.0	-	-	-	pF
			-	-	-	-	20	-	-	-	pF
			-	-	-	-	0.3	-	-	-	pF
Quiescent Current (Per Package)	I <sub>DD</sub>	-	5.0	-	1.0	-	0.001	1.0	-	60	µAdc
			10	-	1.0	-	0.002	1.0	-	60	µAdc
			15	-	2.0	-	0.003	2.0	-	120	µAdc
"ON" Reststance (V <sub>C</sub> = V <sub>DD</sub> , R <sub>L</sub> = 10Ωk) (V <sub>in</sub> = +5.0Vdc) (V <sub>in</sub> = -5.0Vdc) (V <sub>in</sub> = ±0.25Vdc) (V <sub>in</sub> = +7.5Vdc) (V <sub>in</sub> = -7.5Vdc) (V <sub>in</sub> = ±0.25Vdc) (V <sub>in</sub> = +10Vdc) (V <sub>in</sub> = +0.25Vdc) (V <sub>in</sub> = +5.6Vdc) (V <sub>in</sub> = +15Vdc) (V <sub>in</sub> = +0.25Vdc) (V <sub>in</sub> = +9.3Vdc)	R <sub>ON</sub>	-5.0	5.0	-	400	-	200	480	-	640	Ω
				-	400	-	200	480	-	640	Ω
				-	400	-	190	480	-	640	Ω
		-7.5	7.5	-	240	-	160	270	-	400	Ω
				-	240	-	160	270	-	400	Ω
				-	240	-	120	270	-	400	Ω
		0	10	-	400	-	180	480	-	640	Ω
				-	400	-	180	480	-	640	Ω
				-	400	-	220	480	-	640	Ω
		0	15	-	250	-	180	270	-	400	Ω
				-	250	-	180	270	-	400	Ω
				-	250	-	215	270	-	400	Ω
Δ"ON" Reststance Between any 2 circuits in a common package (V <sub>in</sub> = ±5.0Vdc) (V <sub>in</sub> = ±7.5Vdc)	ΔR <sub>ON</sub>	-	-5.0	5.0	-	-	-	15	-	-	Ω
			-7.5	7.5	-	-	-	10	-	-	Ω

Note 2. Data labeled "Typ" is not to be used for design purposes but is intended as an indication of the device's potential performance.

Note 3. The formulas given are for the typical characteristics only at +25°C.

Note 4. Noise immunity specified for worst-case input combination.

Noise margin for both "1" and "0" = 1.0Vdc min @ V<sub>DD</sub> = 5Vdc  
2.0Vdc min @ V<sub>DD</sub> = 10Vdc  
2.5Vdc min @ V<sub>DD</sub> = 15Vdc

**Switching Characteristics:** ( $T_A = +25^\circ\text{C}$ , Note 2)

Parameter	Symbol	V <sub>SS</sub> Vdc	V <sub>DD</sub> Vdc	Min	Typ	Max	Unit		
Propagation Delay Time, V <sub>in</sub> to V <sub>out</sub> (C <sub>L</sub> = 50pF, R <sub>L</sub> = 1kΩ)	t <sub>PLH</sub> , t <sub>PHL</sub>	0.0	5.0	-	20	40	ns		
				10	-	10	20	ns	
				15	-	8.0	15	ns	
Propagation Delay Time, Control to Output V <sub>in</sub> = V <sub>DD</sub> or V <sub>SS</sub> (V <sub>in</sub> ≤ 10Vdc, C <sub>L</sub> = 50pF, R <sub>L</sub> = 1kΩ)	t <sub>PHL</sub> , t <sub>PLH</sub>	0.0	5.0	-	200	400	ns		
				10	-	80	160	ns	
				15	-	50	120	ns	
Crosstalk, Control to Output (C <sub>L</sub> = 50pF, R <sub>L</sub> = 1kΩ, R <sub>out</sub> = 10kΩ)	-	0.0	5.0	-	5.0	-	mV		
				10	-	5.0	-	mV	
				15	-	5.0	-	mV	
Maximum Control Input Pulse Frequency (C <sub>L</sub> = 50pF, R <sub>L</sub> = 1kΩ)	-	0.0	5.0	-	5.0	-	MHz		
				10	-	10	-	MHz	
				15	-	12	-	MHz	
Noise Voltage (f = 100Hz)  (f = 100kHz)	-	0.0	5.0	-	24	-	nV/ $\sqrt{\text{Cycle}}$		
				10	-	25	-	nV/ $\sqrt{\text{Cycle}}$	
				15	-	30	-	nV/ $\sqrt{\text{Cycle}}$	
				5.0	-	12	-	nV/ $\sqrt{\text{Cycle}}$	
				10	-	12	-	nV/ $\sqrt{\text{Cycle}}$	
				15	-	15	-	nV/ $\sqrt{\text{Cycle}}$	
Sine Wave (Distortion) (V <sub>in</sub> = 1.77Vdc, RMS Centered at 0.0Vdc, R <sub>L</sub> = 10kΩ, f = 1kHz)	-	-5.0	5.0	-	0.36	-	%		
Input/Output Leakage Current (V <sub>in</sub> = +5.0Vdc, V <sub>out</sub> = -5.0Vdc) (V <sub>in</sub> = -5.0Vdc, V <sub>out</sub> = +5.0Vdc) (V <sub>in</sub> = +7.5Vdc, V <sub>out</sub> = -7.5Vdc) (V <sub>in</sub> = -7.5Vdc, V <sub>out</sub> = +7.5Vdc)	-	-5.0	5.0	-	±0.001	±125	nA		
				-5.0	5.0	-	±0.001	±125	nA
				-7.5	7.5	-	±0.0015	±250	nA
				-7.5	7.5	-	±0.0015	±250	nA
Insertion Loss (V <sub>in</sub> = 1.77Vdc RMS Centered at 0.0Vdc, f = 1Mhz, I <sub>loss</sub> = 20 Log <sub>10</sub> V <sub>out</sub> /V <sub>in</sub> ) (R <sub>L</sub> = 1kΩ) (R <sub>L</sub> = 10kΩ) (R <sub>L</sub> = 100kΩ) (R <sub>L</sub> = 1MΩ)	-	-5.0	5.0	-	2.0	-	dB		
				-	0.8	-	dB		
				-	0.25	-	dB		
				-	0.01	-	dB		

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Note 3. The formulas given are for the typical characteristics only at +25°C.

**Switching Characteristics (Cont'd):** ( $T_A = +25^\circ\text{C}$ , Note 2)

Parameter	Symbol	V <sub>SS</sub> Vdc	V <sub>DD</sub> Vdc	Min	Typ	Max	Unit
Bandwidth (-3dB) ( $V_{in} = 1.77\text{Vdc}$ RMS Centered at 0.0Vdc) ( $R_L = 1\text{k}\Omega$ ) ( $R_L = 10\text{k}\Omega$ ) ( $R_L = 100\text{k}\Omega$ ) ( $R_L = 1\text{M}\Omega$ )	BW	-5.0	5.0	-	35	-	MHz
				-	28	-	MHz
				-	27	-	MHz
				-	26	-	MHz
Feedthrough and Crosstalk ( $20 \text{Log}_{10} V_{out}/V_{in} = -50\text{dB}$ ) ( $R_L = 1\text{k}\Omega$ ) ( $R_L = 10\text{k}\Omega$ ) ( $R_L = 100\text{k}\Omega$ ) ( $R_L = 1\text{M}\Omega$ )	-	-5.0	5.0	-	850	-	kHz
				-	100	-	kHz
				-	12	-	kHz
				-	1.5	-	kHz

Note 2. Data labeled “Typ” is not to be used for design purposes but is intended as an indication of the device’s potential performance.

Note 3. The formulas given are for the typical characteristics only at +25°C.

**Truth Table**

ST <sub>X</sub>	ST <sub>Y</sub>	B	A	Z	W	Function
1	1	0	0	X0	Y0	Dual 4-Channel Mode 2 Outputs
1	1	0	1	X1	Y1	
1	1	1	0	X2	Y2	
1	1	1	1	X3	Y3	
1	0	0	0	X0	Single 8-Channel Mode 1 Output (Z and W tied together)	
1	0	0	1	X1		
1	0	1	0	X2		
1	0	1	1	X3		
0	1	0	0	Y0	Single 8-Channel Mode 1 Output (Z and W tied together)	
0	1	0	1	Y1		
0	1	1	0	Y2		
0	1	1	1	Y3		
0	0	DC	DC	High Impedance		

DC = Don't Care

### Pin Connection Diagram

